

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of: **Kazuhisa TSUNOI et al.**

Serial No.: Div. of 09/045,115

Group Art Unit: Not Yet Assigned

Filed: March 14, 2001

Examiner: Not Yet Assigned

For: MOUNTING METHOD OF SEMICONDUCTOR DEVICE

4 / IDS  
E. Willis  
5-31-01

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 CFR 1.97(b)**

Director of Patents and Trademarks  
Washington, D.C. 20231

March 14, 2001

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent (or grandparent) application Serial No. 09/045,115.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, WESTERMAN, HATTORI,  
McLELAND & NAUGHTON, LLP

*TEB*

Thomas E. Brown  
Attorney for Applicants  
Registration No. 44,450

Attorney Docket No. **980404A**  
1725 K Street, N.W., Suite 1000  
Washington, D.C. 20006  
Tel: (202) 659-2930  
Fax: (202) 887-0357  
Enclosures: PTO-1449  
TEB:jjnj

